

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Satoshi TORII**

Serial Number: **Not Yet Assigned**

Filed: **July 15, 2003**

For: **NONVOLATILE SEMICONDUCTOR MEMORY AND METHOD OF OPERATING
THE SAME**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 15, 2003

Sir:

In compliance with 37 CFR 1.56, Applicant calls to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

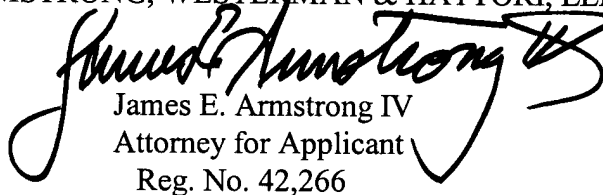
A copy of each of the references are enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge

Deposit Account No. 01-2340.

Respectfully submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP


James E. Armstrong IV
Attorney for Applicant
Reg. No. 42,266

JAM/jaz
Atty. Docket No. **030842**
Suite 1000
1725 K Street, N.W.
Washington, D.C. 20006
(202) 659-2930



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PATENT TRADEMARK OFFICE

Enclosures: PTO-1449; References (2)

INFORMATION DISCLOSURE STATEMENT PTO-1449	Atty. Docket No. 030842	Serial No. New Application
	Applicant(s): Satoshi TORII	
	Filing Date: July 15, 2003	Group Art Unit: Not Yet Assigned

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					

FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Translation (Yes or No)	
_____	AE	2000-174235	06/23/00	Japan	Yes-Abstract/Discussed in the specification
_____	AF	2001-325793	11/22/01	Japan	Yes-Abstract/Discussed in the specification
_____	AG				
_____	AH				
_____	AI				

OTHER DOCUMENTS

_____	AJ			
_____	AK			
<table border="0" style="width: 100%;"> <tr> <td style="width: 50%;">Examiner</td> <td style="width: 50%;">Date Considered</td> </tr> </table>			Examiner	Date Considered
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